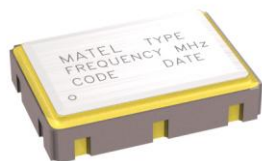


Ceramic SMD VCXO 5X3.2

VC-T1 & VC-T3



FEATURES
1.750 to 54 MHz HCMOS Tri-State Enable
APPLICATIONS
Sonet SDH

Electrical Parameters

Parameters	Conditions	VC-T1	VC-T3
Frequency Range*		1.75 ~ 54 MHz	
Frequency Calibration	At 25°C	±15 ppm	
Frequency Stability	Over T _{OPR}	± 15 ppm, ± 25 ppm, ± 50 ppm	
Stability vs.power change	VDD ± 5%	±5 ppm	
Stability vs.load change	15pF ±10%	±3 ppm	
Pullability	Over Control Voltage Range	±50ppm, ±100ppm, ±200ppm	±50ppm, ±100ppm, ±150ppm
Control Voltage Range		0.5~4.5V	0~3.3V
Operating Temperature Range		0 ~ 70°C , -40 ~ 85°C	
Storage Temperature Range		-55°C ~ 125°C	
Power Supply Voltage		+5V ± 5%	+3.3V ± 5%
Ageing (First Year)	25°C ± 3°C	± 5 ppm Max	
Supply Current		30 mA Max	
Output Symmetry	At ½ VDD	40/60% (45/55% option)	
Rise Time	20% VDD ~ 80% VDD	8 nS Max	10 nS Max
Fall Time	80% VDD ~ 20% VDD	8 nS Max	10 nS Max
Output Voltage		90% VDD Min 10% VDD Max	
Output Load HCMOS Load		15 pF Max	
Start-up Time		10 mS Max	

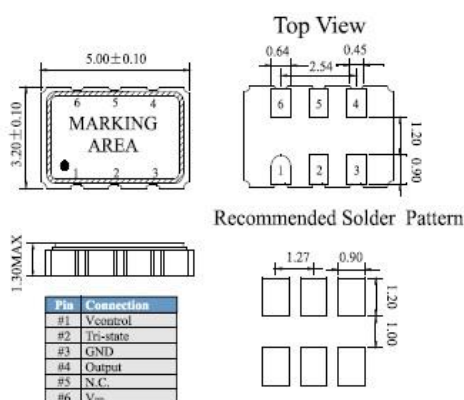
Ordering Code Stability

Stability*	Code	Pulling Range	Code	Temperature Range	Code
± 15 ppm	A	± 75	075	0 + 70°C	N
± 25 ppm	C	± 100	100	- 10 + 70°C	M
± 50 ppm	E	±150	150	- 20 + 70°C	E
		±200	200	- 40 + 85°C	W

Options

Option	Code
Symmetry 45/55%	T

Mechanical Dimensions (mm)



SMD VCXO

TYPE	VC-T1 & VC-T3	REVISION	03	CHECKED	PB	DATE	01/04/2015
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All specifications are subject to change without notice