SMD Oscillator CXO 3.2X2.5
HXO-SF & HXO-SC & HXO-SD & HXO-SE

www.matel-piezo.fr

FEATURES
1 to 125 MHz HCMOS/TTL compatible Tri-State Enable/Disable
APPLICATIONS
Clocking Datacomms PCMCIA

Electrical Parameters

Parameters	Conditions	HXO-SF	HXO-SC	HXO-SD	HXO-SE
Frequency Range		1 to 54 MHz	1 to 125 MHz		
Frequency Stability*	All Conditions*	± 25 ppm, ± 50 ppm, ± 100 ppm			
Operating Temperature Range		-10 ~ 70°C, -20 ~ 70°C, -40 ~ 85°C			
Storage Temperature Range		-55°C ~ 125°C			
Power Supply Voltage		+5V ± 10%	+3.3V ± 10%	+2.8V ± 10%	+1.8V ± 10%
Supply Current	1 to 9.999 MHz	15 mA Max	8 mA Max	7 mA Max	6 mA Max
	10 to 34.999 MHz	20 mA Max	10 mA Max	8 mA Max	7 mA Max
	35 to 49.999 MHz	35 mA Max	25 mA Max	20 mA Max	15 mA Max
	50 to 54 MHz	40 mA Max	35 mA Max	30 mA Max	25 mA Max
Output Symmetry	At ½ VDD	40/60% (45/55% option)			
Rise Time	10% VDD ~ 90% VDD	5 nS Max		6 nS Max	7 nS Max
Fall Time	90% VDD ~ 10% VDD	5 nS Max		6 nS Max	7 nS Max
Output Voltage		90% VDD Min			
		10% VDD Max			
Output Load HCMOS Load		15 pF Max			
Start-up Time		10 mS Max			
Ageing (First Year)	25°C ± 3°C	± 2 ppm Max			

Ordering Code Stability

Stability*	Code		Temperature Range	Code
± 25 ppm	A		- 10 + 70°C	M
± 50 ppm	B		- 20 + 70°C	E
± 100 ppm	C		- 40 + 85°C	W

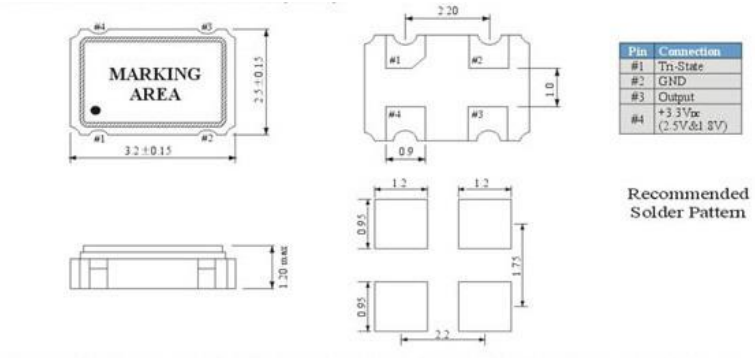
**Includes: 25°C calibration, operating temperature range, input voltage and load change, ageing, shock and vibration*

Example: HXO-SCBWT 48MHz

Options

Option	Code
Symmetry 45/55%	T

Mechanical Dimensions (mm)



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Note : A 0.01uF bypass capacitor should be place between Vdd (Pin 4) and GDN (pin 2) to minimize power supply line noise